

Abstract

Switching arrangement for interconnecting electrolytic capacitors that comprises an electronic switch formed by a semiconductor device and a delay member. The semiconductor
5 device has a control input which is, through an RC-type delay member, connected to a control input (VS) supplying the switching signal. The switching arrangement has a predetermined switching delay. The semiconductor device is a field effect transistor (FT), whose main circuit is coupled through an inductive element (L), supplying a second delay, to
10 the capacitor (C1) to be switched. The inductive element (L) is a conductor (10) of determined length surrounded by a high-frequency ferrite core (11, 12). The delay effected by the RC member ensures only that fraction of the switching delay at which the device is loaded within its permissible load limit and the remaining delay is supplied by the inductive element (L).

15 Figures 1 and 2